

Figure 1 is a perspective view of a color display panel assembly. The assembly consists of a top panel (6) and a bottom panel (11). The top panel (6) is a color filter substrate with a grid of color filters (G, B, R) on a substrate (5). The bottom panel (11) is a pixel electrode substrate with a grid of pixel electrodes (14) on a substrate (13). The pixel electrodes (14) are connected to a common electrode (15) and a gate electrode (17). The assembly is shown in a perspective view with labels 6, 7, 18, 14, P, 17, 11, 13, 15, T, and 22.

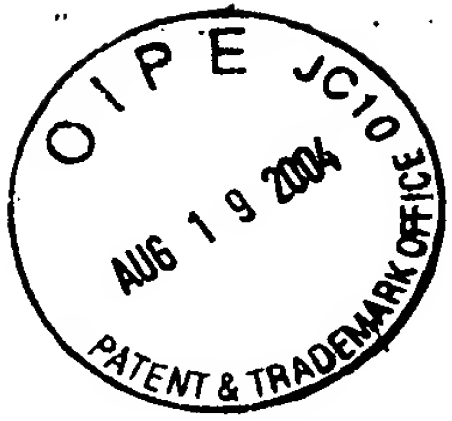
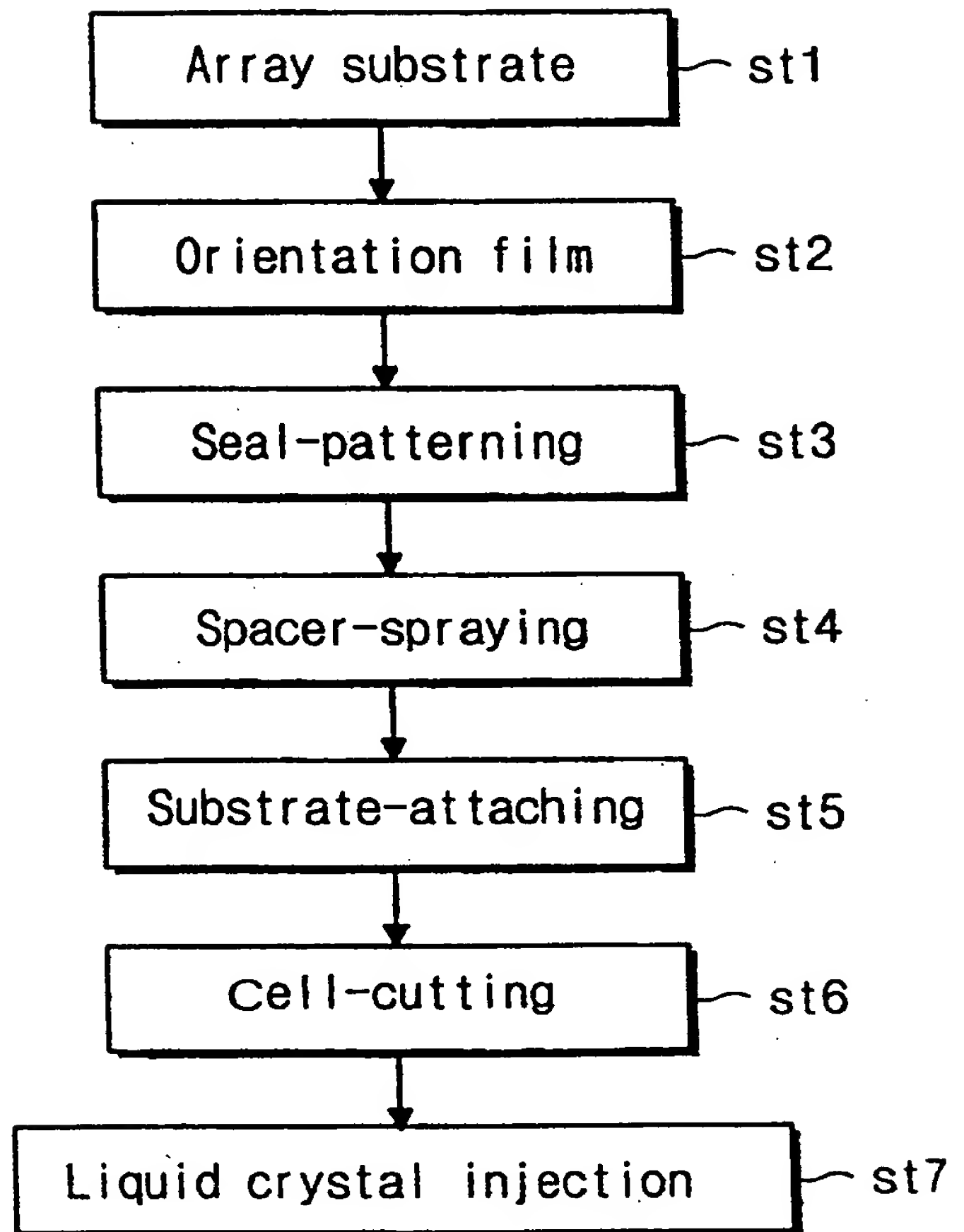


FIG.2
(RELATED ART)



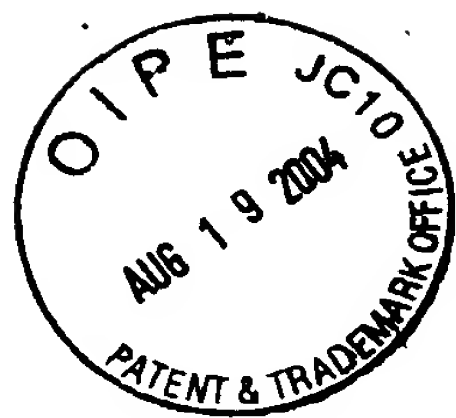


FIG.3
(RELATED ART)

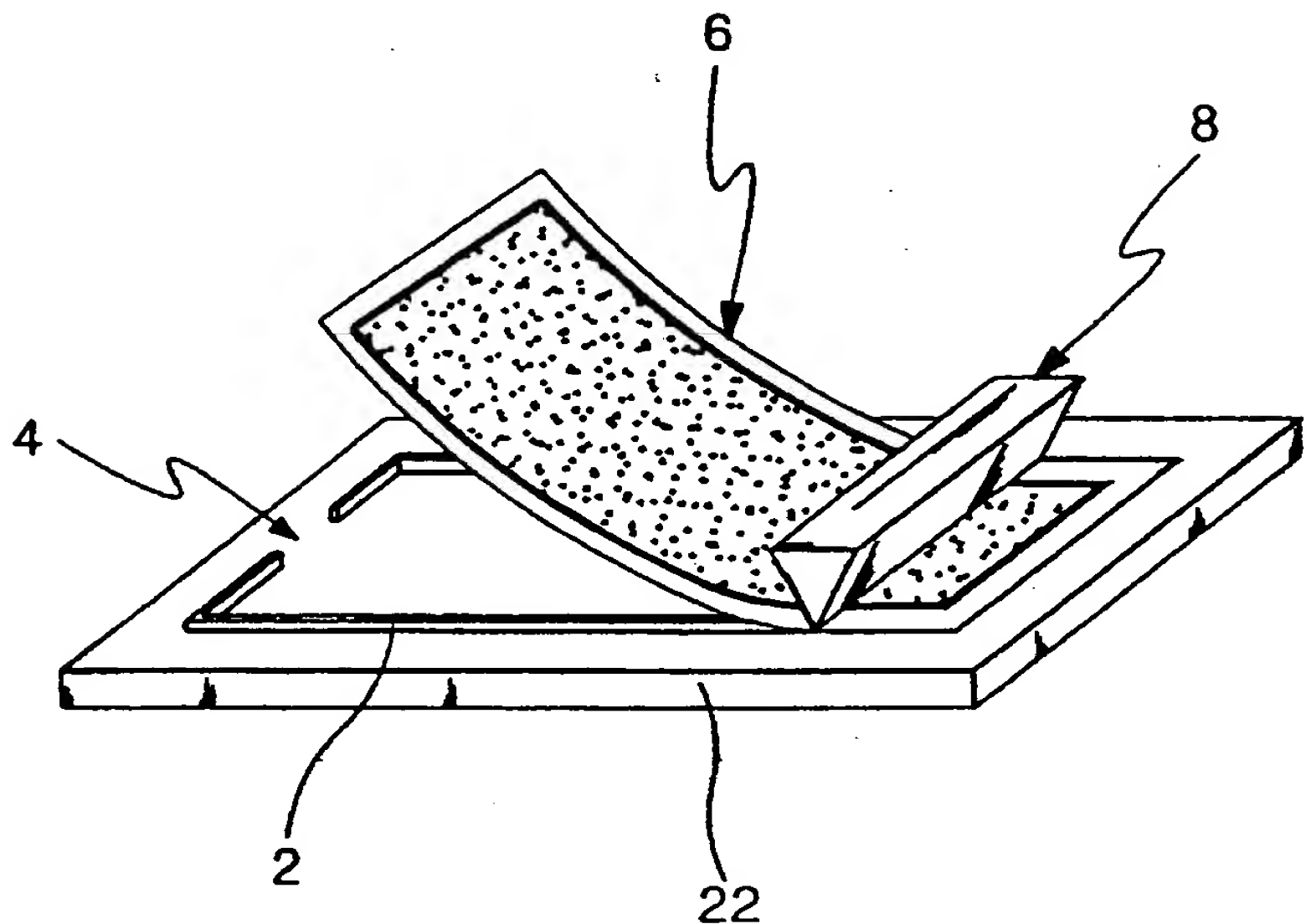
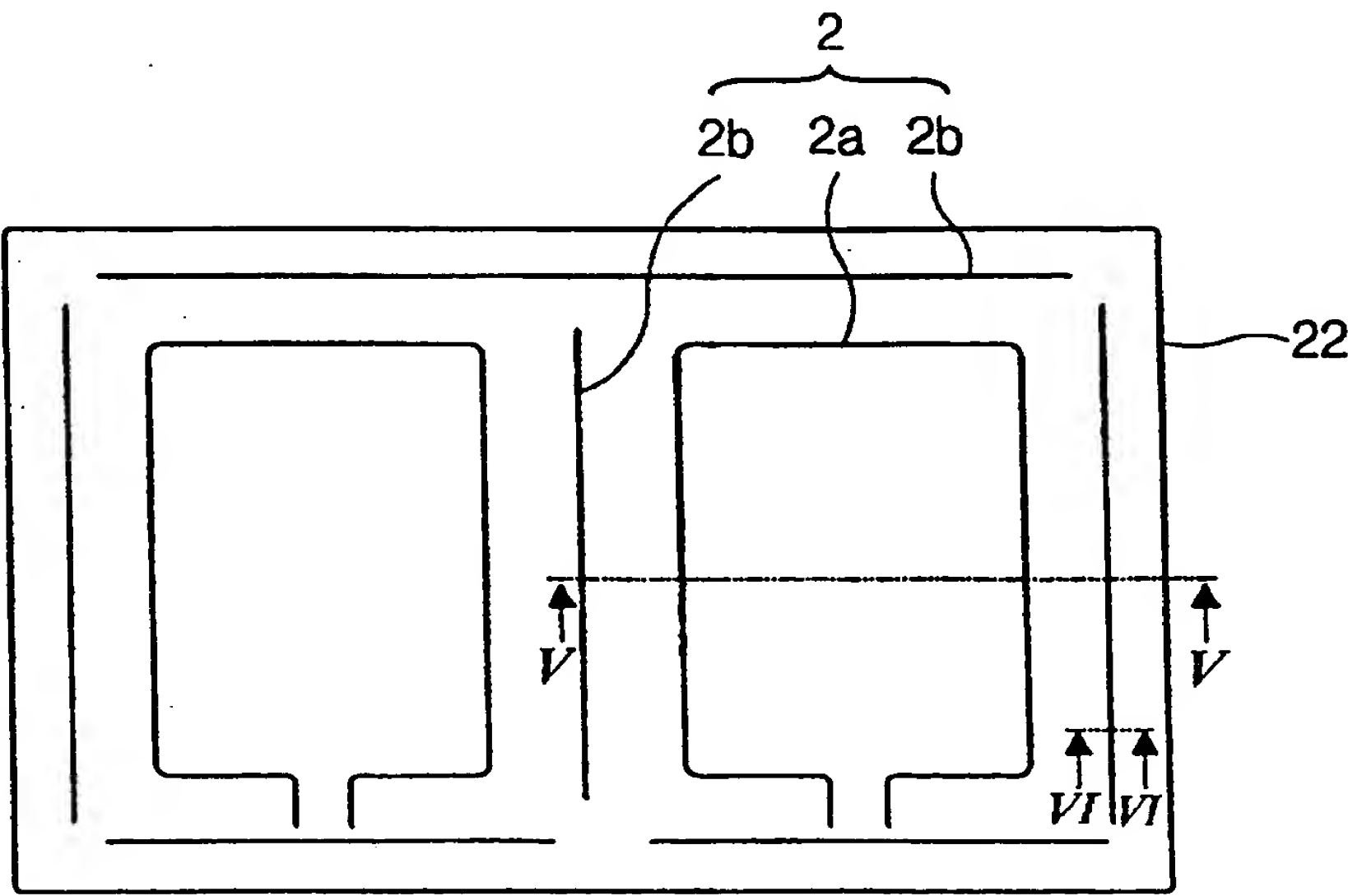


FIG.4
(RELATED ART)



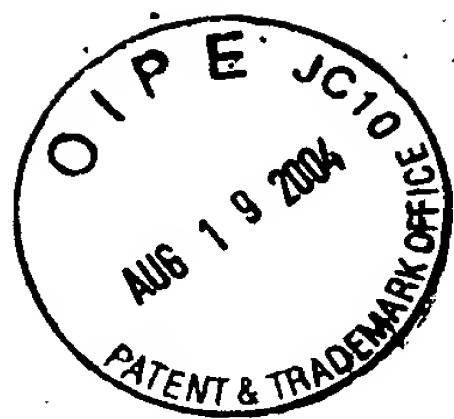


FIG.5
(RELATED ART)

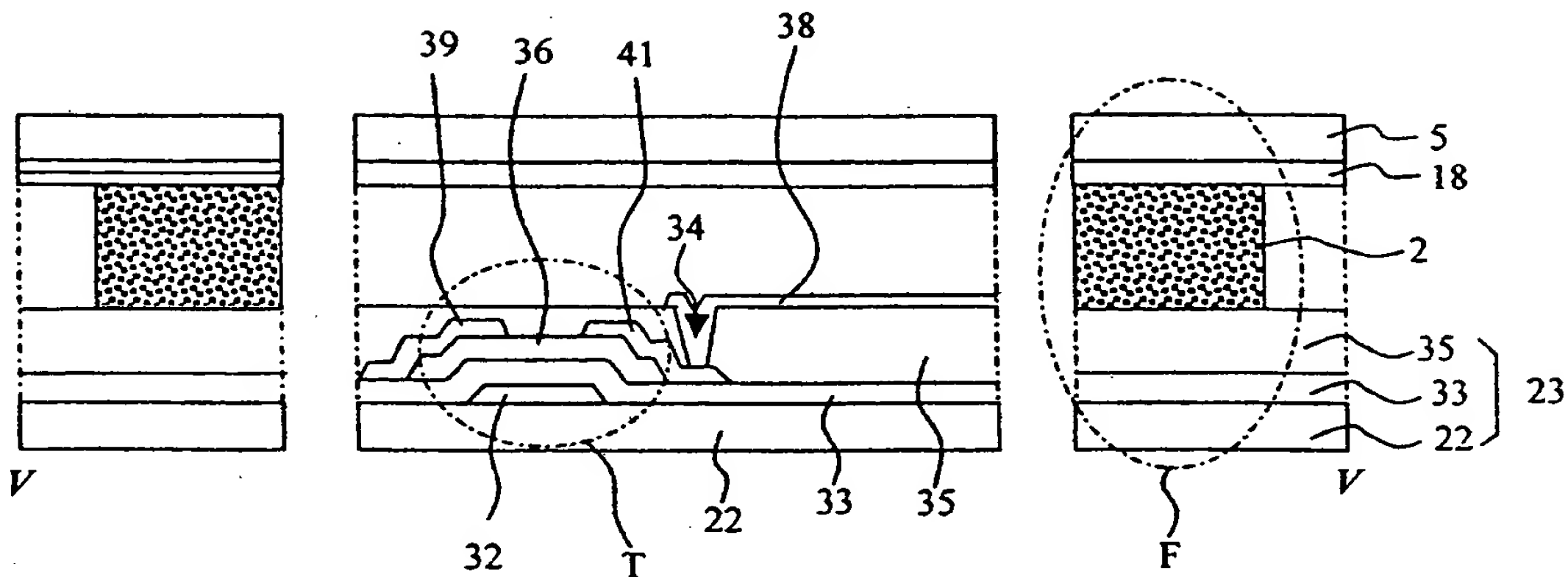
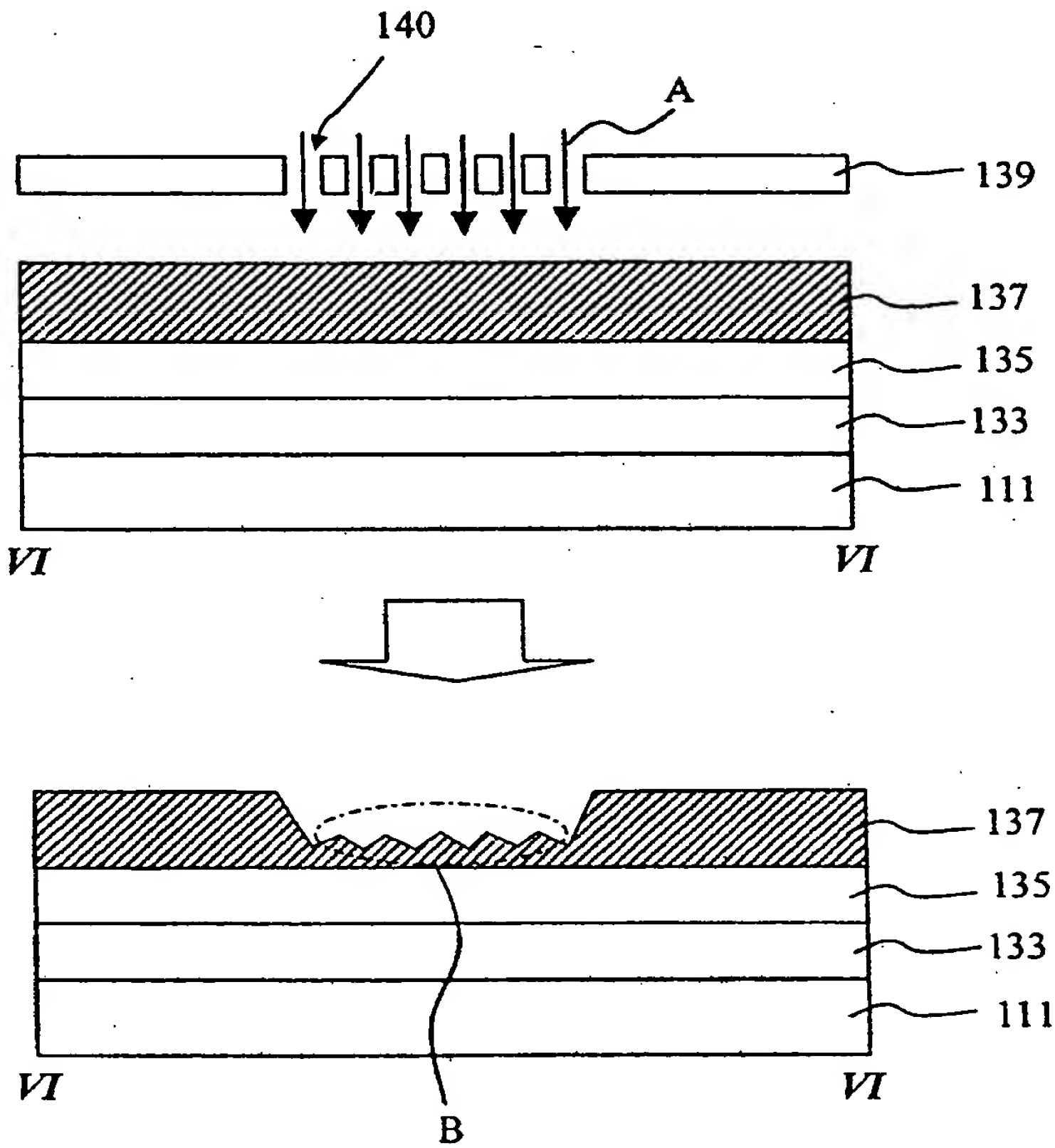


FIG.6A



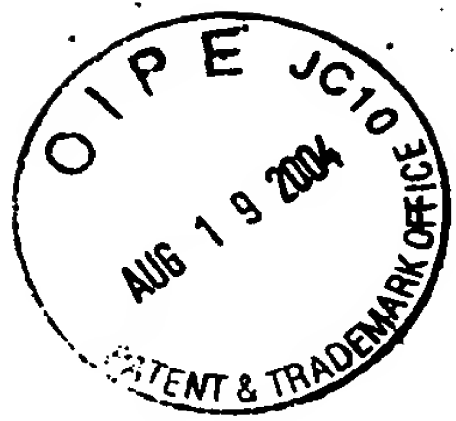


FIG.6B

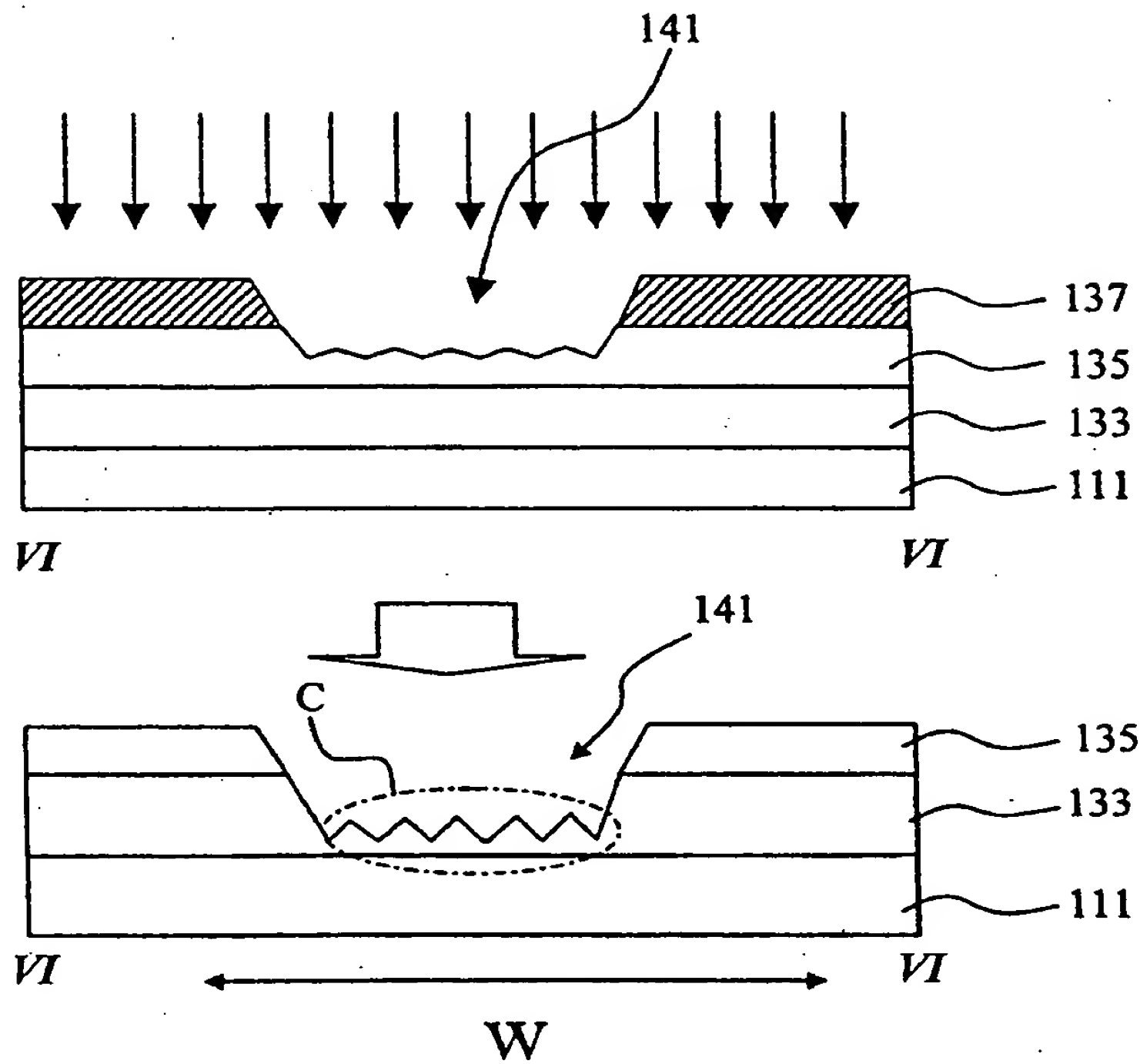


FIG.6C

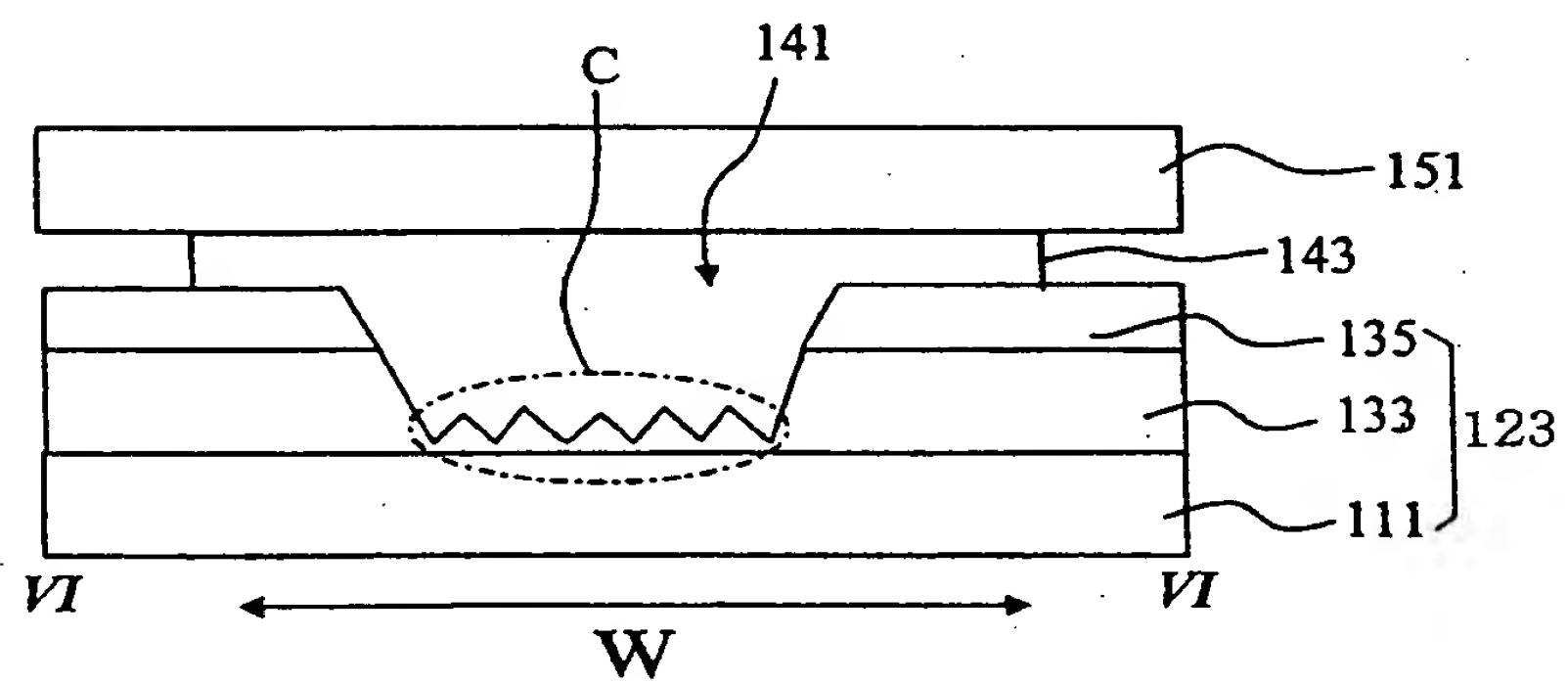


FIG.7A

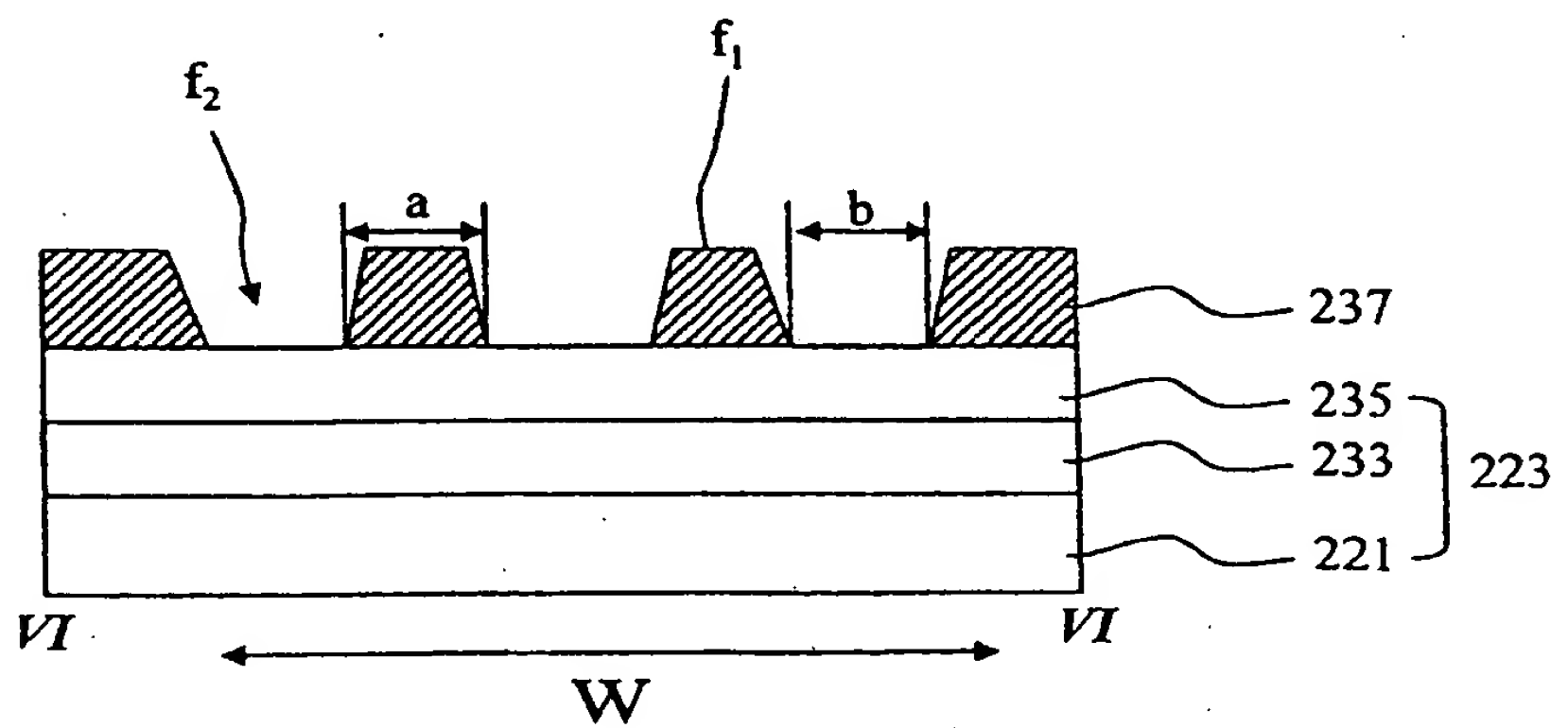




FIG.7B

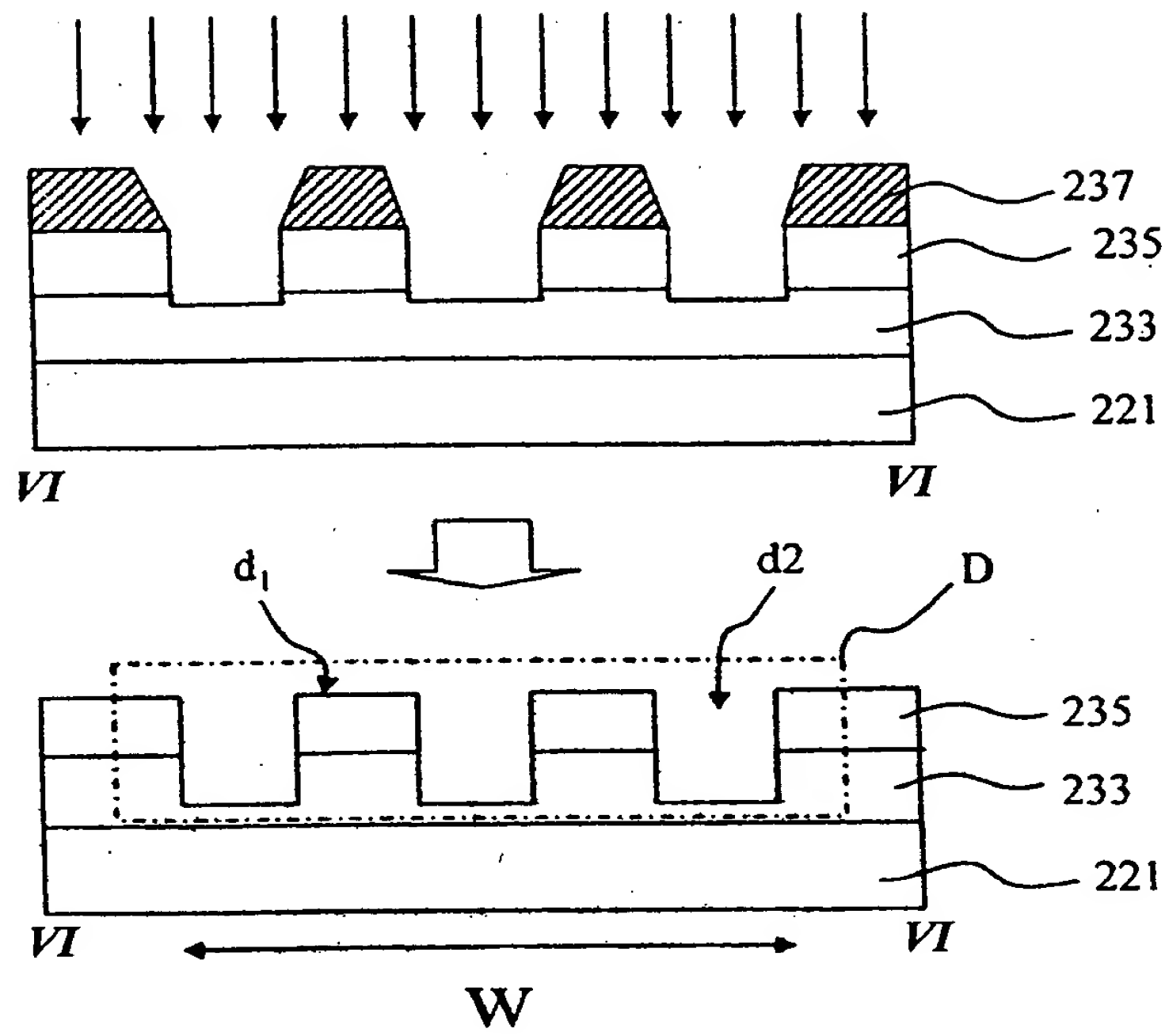


FIG.7C

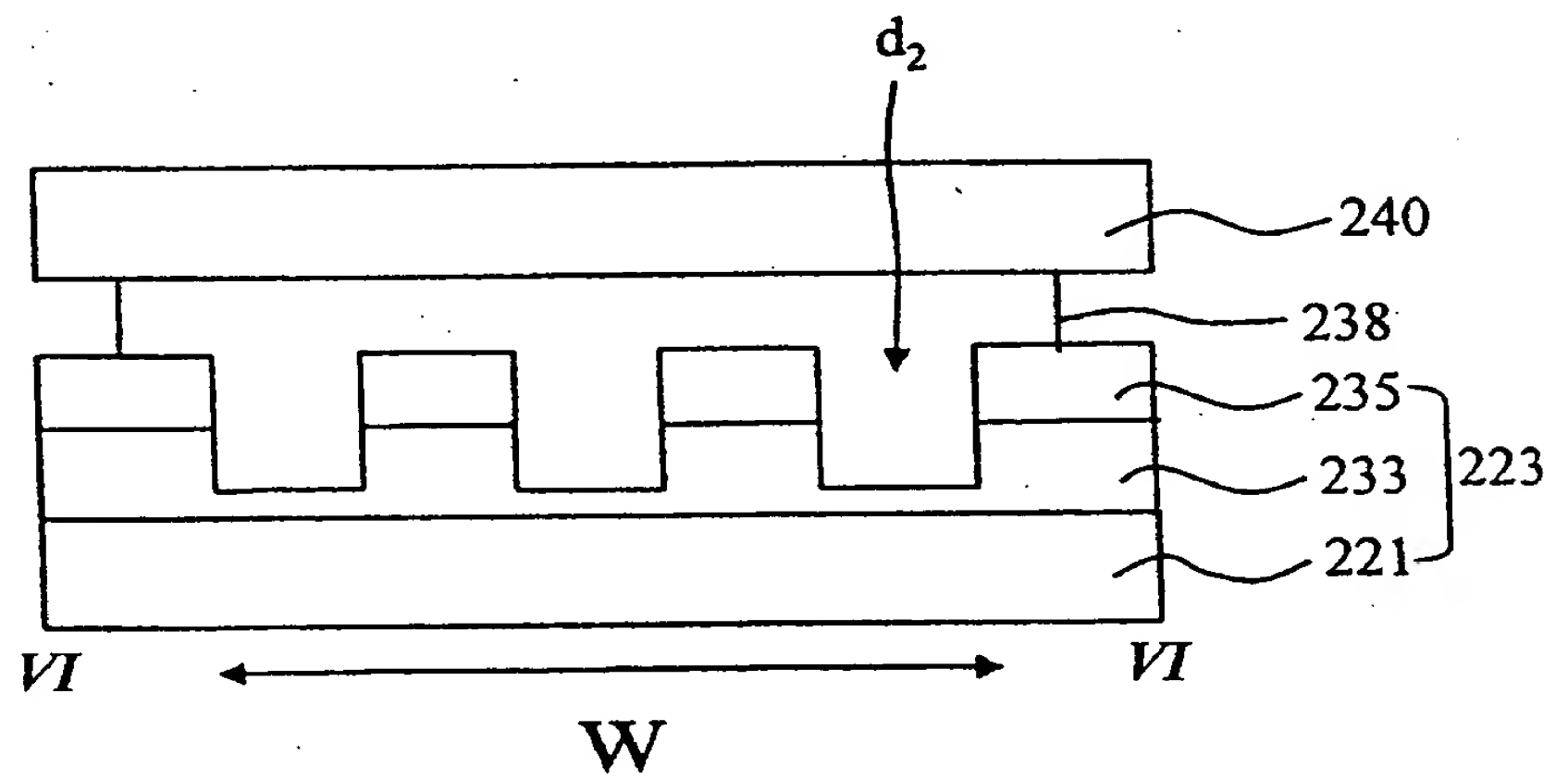
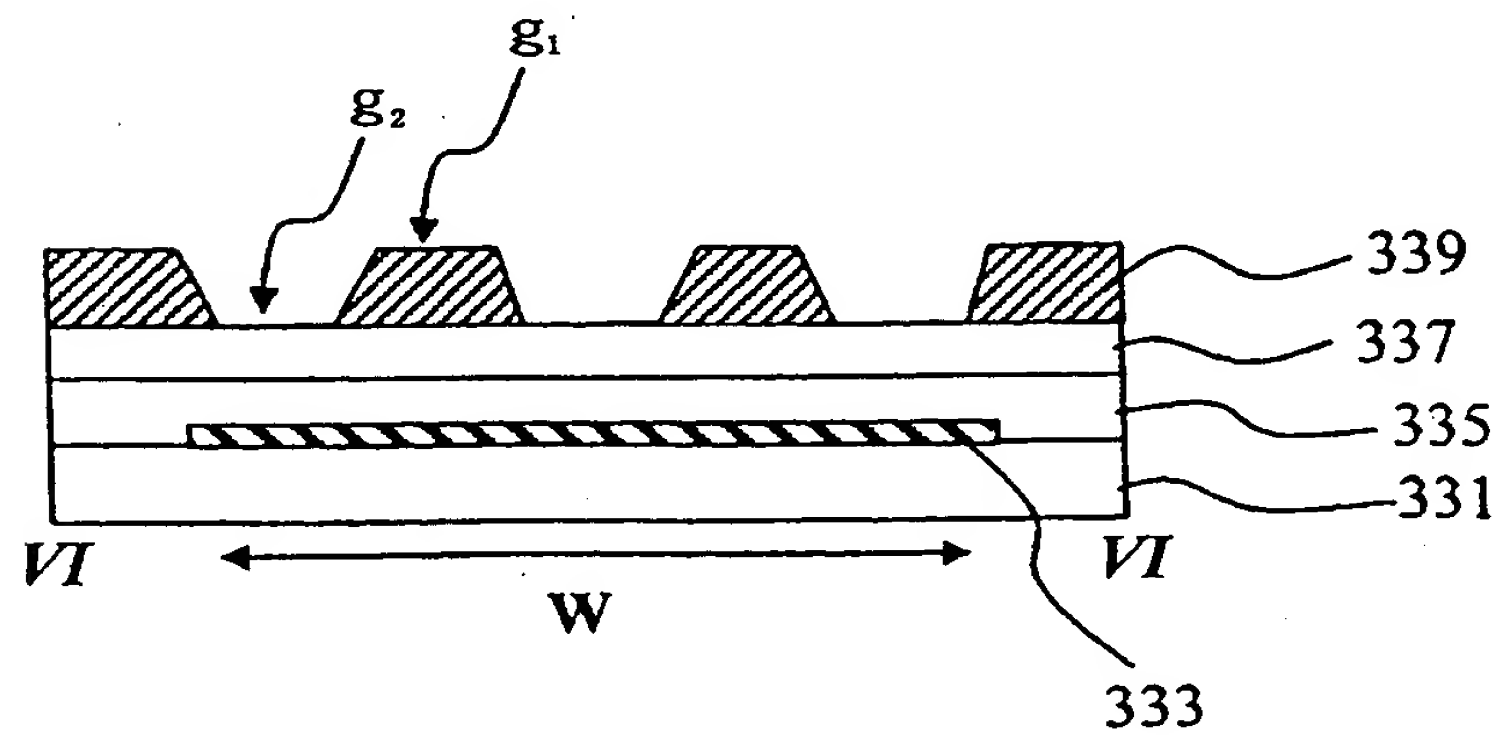


FIG.8A



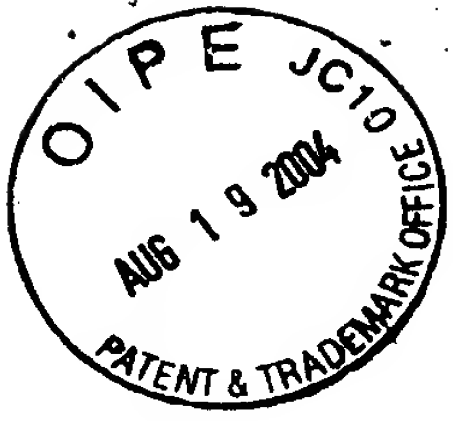


FIG.8B

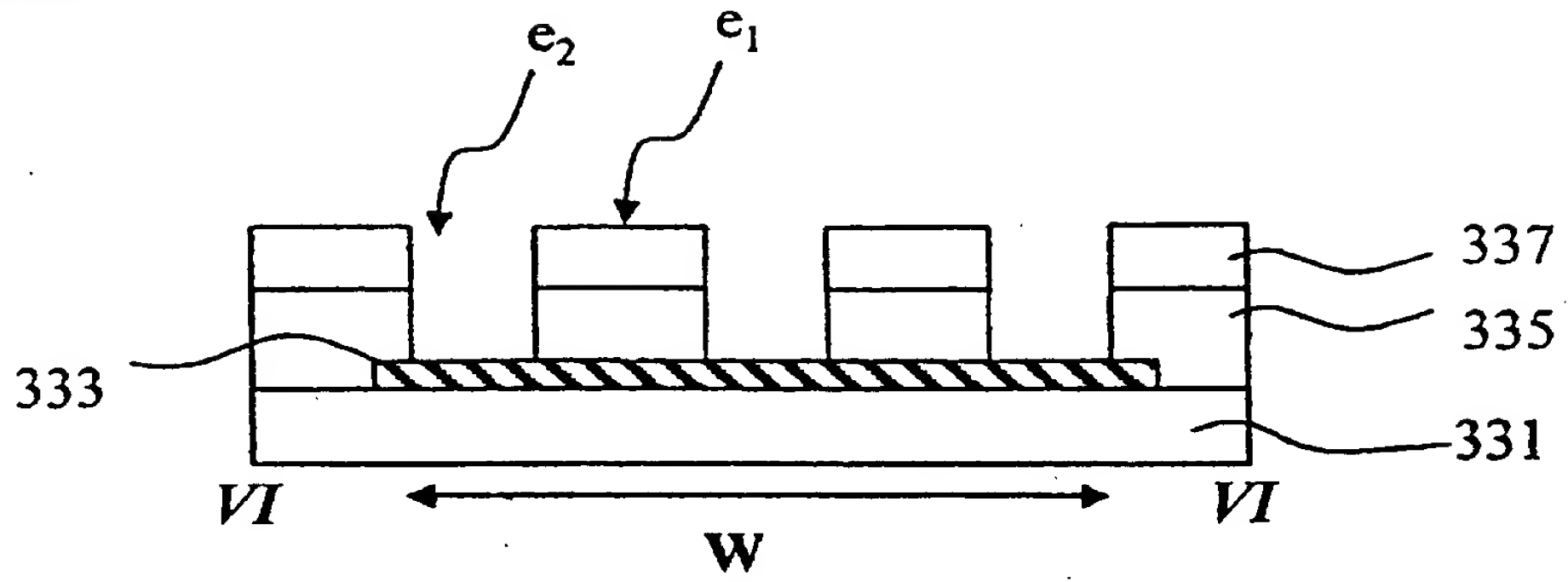


FIG.8C

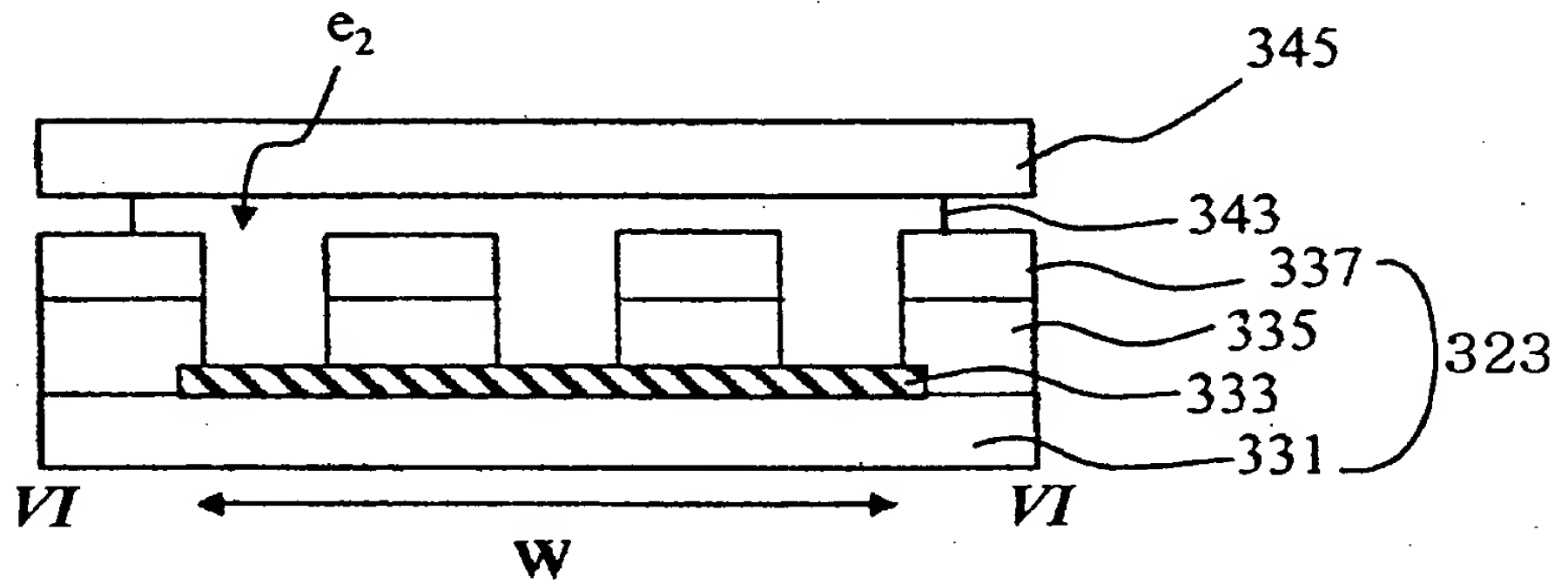
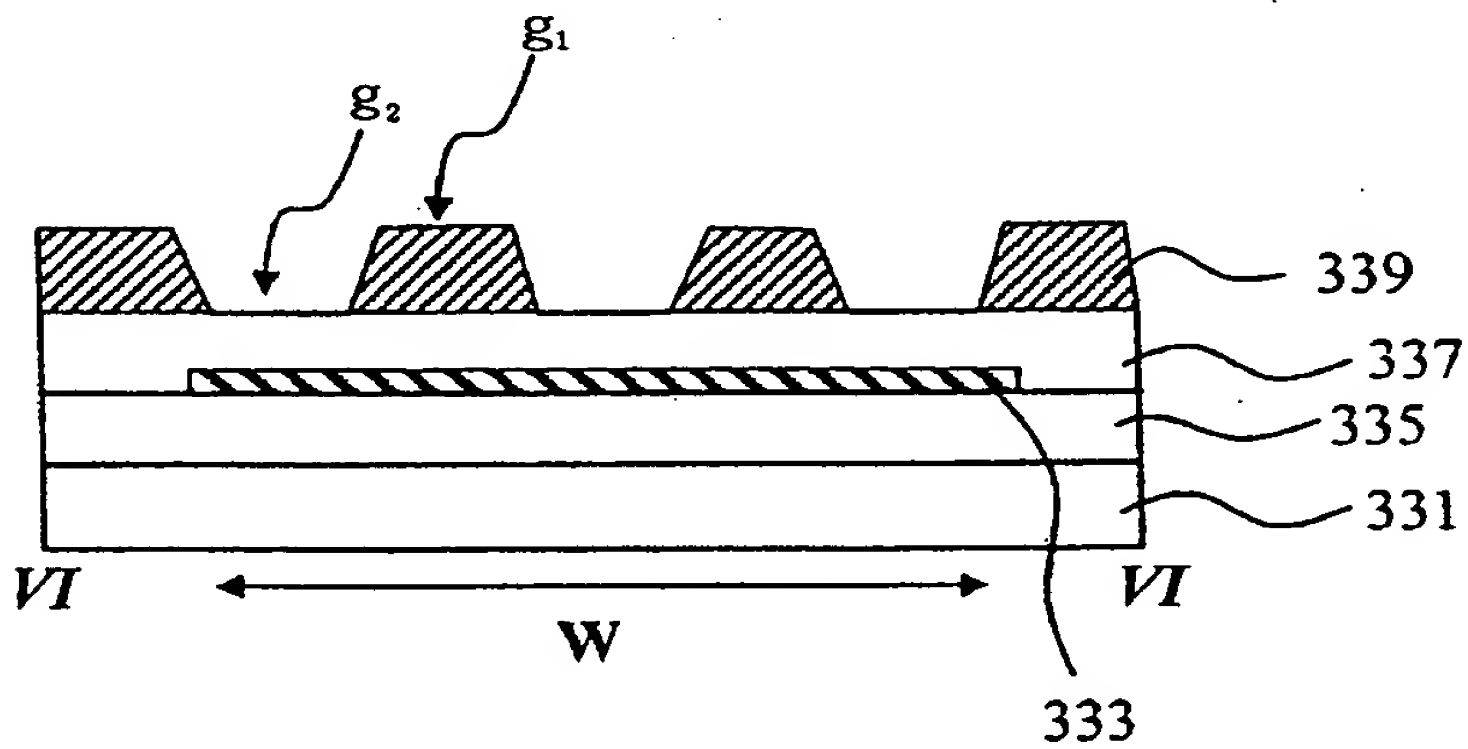


FIG.9A



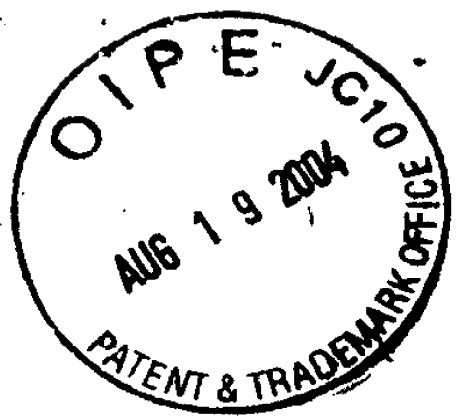


FIG.9B

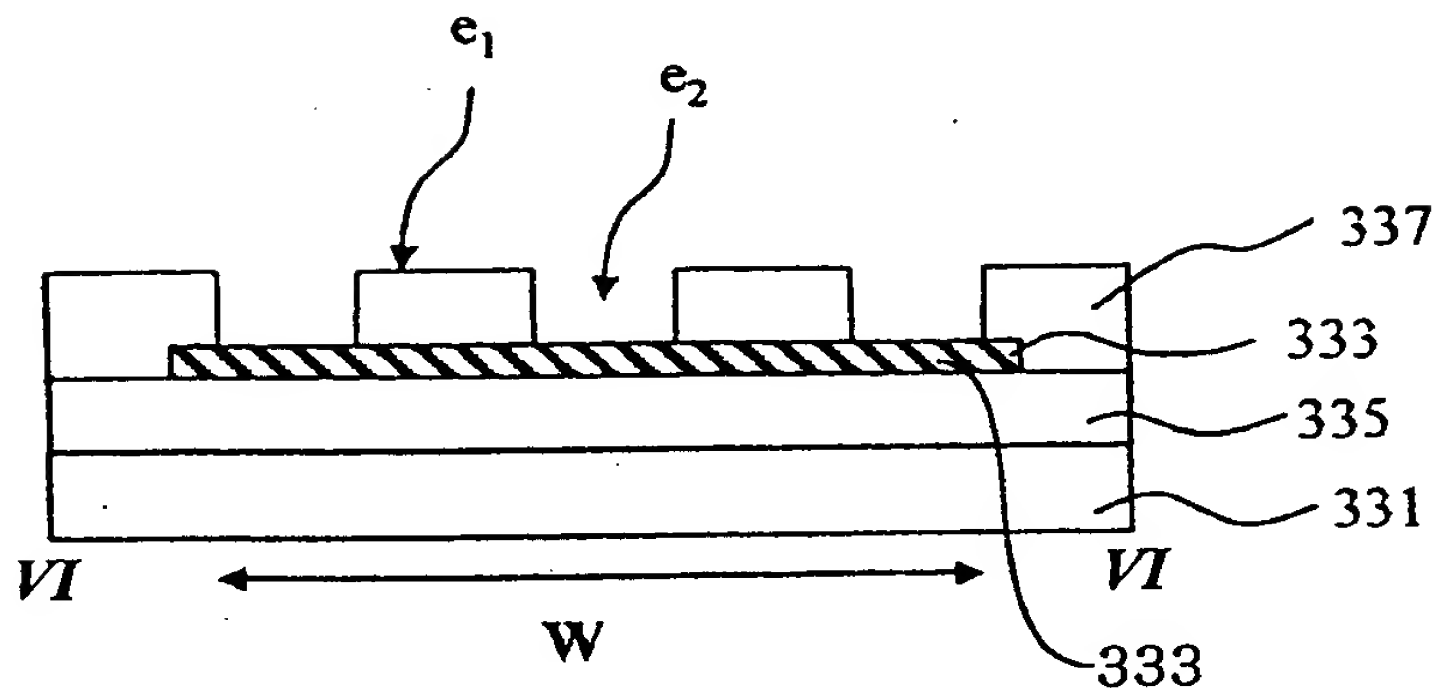


FIG.9C

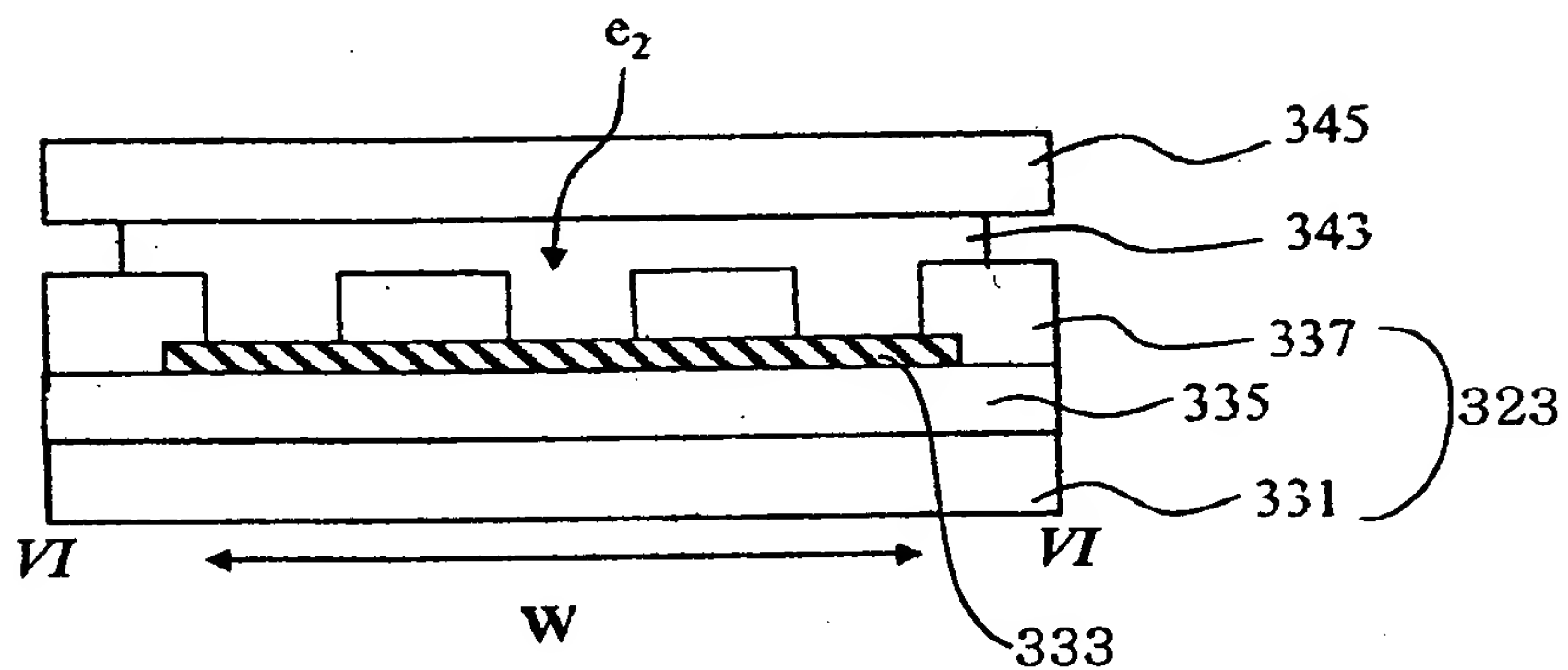


FIG.10A

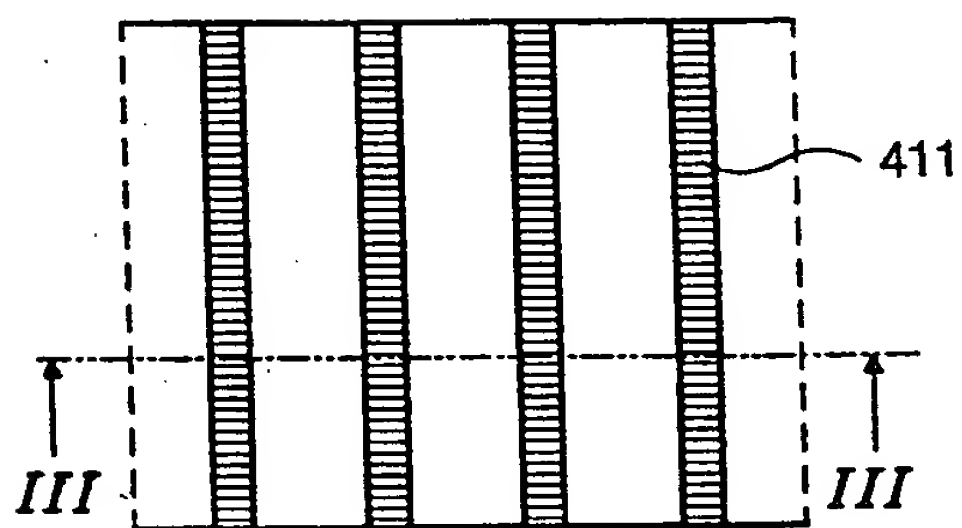


FIG.10B

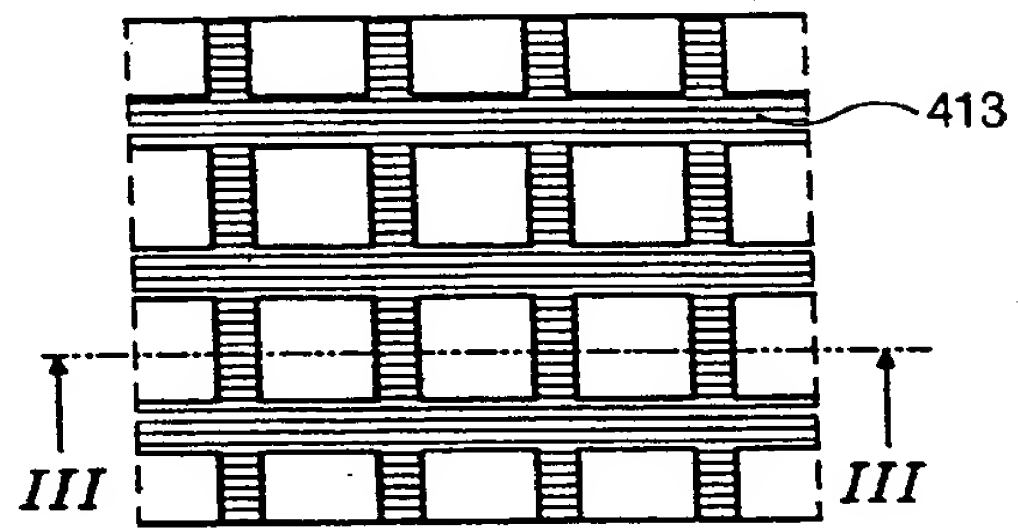


FIG.10C

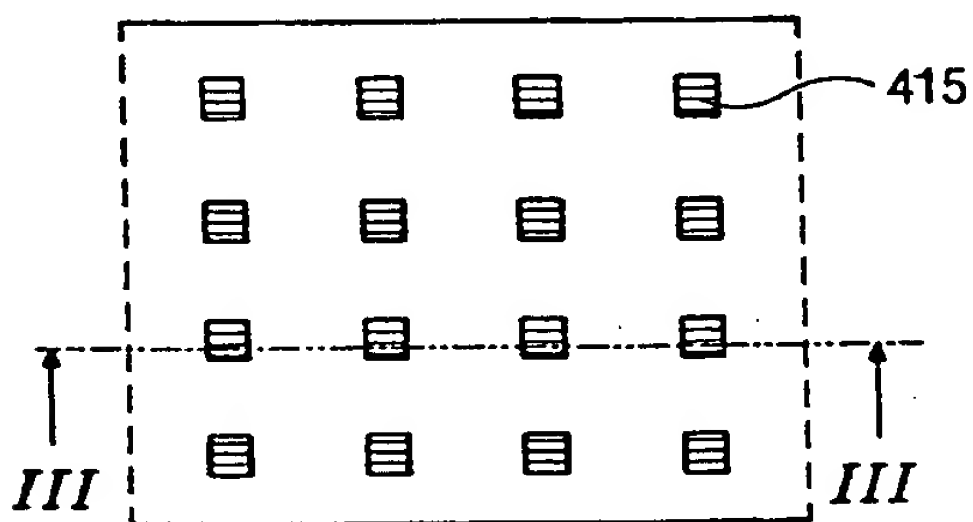


FIG.10D

